

Name of Teaching Staff	:	Prof. Kaushal K. Pandey	
Designation	:	Assistant Professor	
Department	:	Electronics & Telecommunication Engineering	
Date of Joining the Institution	:	01.10.2026	
Email ID	:	mail2pandeykk@gmail.com	
Office Contact	:	022-42331331	
Google Scholar Link	:		
Researchgate Link:			
ORCID			
Qualifications with Class / Grade	:	1. M.E. – Electronics & Telecommunication Engineering (Digital Electronics) from Kavayitri Bahinabai Chaudhari North Maharashtra University, Jalgaon, in May 2014 , 1 st class with Distinction 75.35%. 2. B.E. (Electronics & Telecomm. Engineering) from Dr. Babasaheb Ambedkar Marathwada University, Aurangabad in May 1999.	
Total Experience in Years	:	<u>Teaching: 27 years</u> 1. Assistant Professor, Thakur Shree DPS college of Engineering and Management, Vasai, 01.07.2026 to 30-09-2026. 2. Assistant Professor, Shivajirao S Jondhale College of Engineering, Dombivli, 12-08-1999 to 30-06-2026. <u>Industry:</u> -- <u>Research:</u> --	
Papers Published in Journal:	:	<u>National: 0</u> <u>International: 3</u> [1] Review on Fault diagnosis in Induction Motor, International Journal of computer application, Medha-, 2012. [2] Estimating Parameters of a three phase induction motor using Matlab/Simulink, International journal of scientific and engineering research, Volume 4, Issue 12, December 2013. [3] Analysis and simulation of a three-phase induction motor using Clarke's transformation, International journal of Engineering research and Technology, ICONECT-14, Conference proceedings	

Papers Presented in Conferences		[1] Analysis and simulation of a three-phase induction motor using Clarke's transformation, ICONECT-14	
Area of Specialization		Electronics devices and circuits, Digital Electronics	
PhD Guide? Give field & University	:	<u>Field:</u> <u>University:</u>	
PhDs / Projects Guided	:	<u>PhDs :</u> <u>Projects at Masters level:</u>	
Books Published / IPRs / Patents	:		
Professional Memberships	:		
Consultancy Activities	:		
Awards	:		
Grants fetched	:		
Interaction with Professional Institutions	:		
Subjects Taught		<u>UG Level:</u> <u>PG Level:</u>	EDC, DSD, DVLSI, ANN
Projects Guided	:	<u>UG Level:</u> <u>1.</u> <u>2.</u> <u>PG Level:</u> <u>1.</u> <u>2.</u>	
Recommended Students for Higher Education		<u>Name of the Student</u>	<u>University/Industry</u>
Institute/Department Responsibility handled:		➤ Co-ordinator for First year and Direct second year engineering process (FC and SC) ➤ Faculty Coordinator ➤ NAAC Coordinator (Criteria 2)	
Pedagogy Development			